

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. 1876.1260002	APPLICATION NO. To Be Assigned 10/82,978
	APPLICANT Sandeep K. GUPTA	
	FILING DATE February 20, 2004	GROUP To Be Assigned 2816

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
HN	AA1	4,229,692	10/1980	Grogono	324	62	
	AB1	5,199,277	10/1992	Mourat	324	721	
	AC1	5,249,235	09/1993	Wakayama et al.	327	92	
	AD1	5,572,154	11/1996	Rahera et al.	327	92	
	AE1	5,608,814	03/1997	Ornlight et al.	363	60	
	AF1	5,698,999	12/1997	Etoh et al.	327	94	
	AG1	5,736,895	04/1998	Yu et al.	327	554	
	AH1	5,745,002	04/1998	Baschiroto et al.	327	554	
	AI1	5,753,815	05/1998	Murata	73	204/15	
	AJ1	5,838,175	11/1998	Hsieh	327	94	
✓	AK1	5,892,473	04/1999	Shin	341	150	

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1	WO 94/01936	01/1994	PCT	H03K	19/0175	Yes No
	AM1	WO 00/76682 A1	12/2000	PCT	H03K	17/14	Yes No
	AN1	FR 2 793 970	11/2000	France	H03K	17/16	Yes No
	AO1						Yes No
	AP1						Yes No

OTHER (including Author, Title, Date, Pertinent Pages, etc.)			
HN	AR	1	Low-voltage, High-Speed, High-Precision Switched-capacitor Circuits: Slide 5 of 25, from http://kabuki.eecs.berkeley.edu/~abo/slides/quals/quals.5.html , 1 page (April 25, 1996).
HN	AS	1	Low-voltage, High-Speed, High-Precision Switched-capacitor Circuits: Slide 8 of 25, from http://kabuki.eecs.berkeley.edu/~abo/slides/quals/quals.8.html , 1 page (April 25, 1996).
HN	AT	1	Low-voltage, High-Speed, High-Precision Switched-capacitor Circuits: Slide 11 of 25, from http://kabuki.eecs.berkeley.edu/~abo/slides/quals/quals.11.html , 1 page (April 25, 1996).
EXAMINER	HAN L. NGUYEN		DATE CONSIDERED 6/10/04
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.			